



SANKAR POLYTECHNIC COLLEGE

A Government Aided Autonomous Institution Approved by AICTE & Affiliated to DoTE Chennai



DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

E & ICT ACADEMY - PHASE - II

(An Initiative of Ministry of Electronics & Information Technology,
Government of India)

Faculty Development Program (FDP)

On

PCB Design

Design Smart. Build Reliable. Innovate Faster

FROM CONCEPT TO CIRCUIT BOARD

EMPOWERING FACULTY WITH INDUSTRY-READY PCB SKILLS

Departments Eligible
EEE | ECE | EIE



Venue

Sankar Polytechnic College



Dates

23rd to 27th February 2026

PROGRAM HIGHLIGHTS

- ◆ PCB Design Fundamentals & Best Practices
- ◆ Schematic Capture & Layout Techniques
- ◆ Design Rule Check (DRC) & Manufacturing Guidelines
- ◆ Hands-on Training with Industry-Standard Tools
- ◆ Exposure to PCB fabrication & Testing

Organized by

PATRON

Dr. A. SANKARA SUBRAMANIAN

PRINCIPAL, SPC

ADMINISTRATIVE OFFICER

Mr.R.V. SRINIVASAN

CONVENOR

Mr. I. SARAVANAN

HOD, EEE

SPOC

Mrs.V. VIMALA

LECTURER / EEE

PROGRAM CO-ORDINATORS

Mrs.P. NAGALAKSHMI KANNA - Lecturer / EEE

Mr. A. GUNA - Lecturer / EEE

Learn. Design. Implement

ENHANCING TEACHING THROUGH PRACTICAL PCB DESIGN SKILLS